

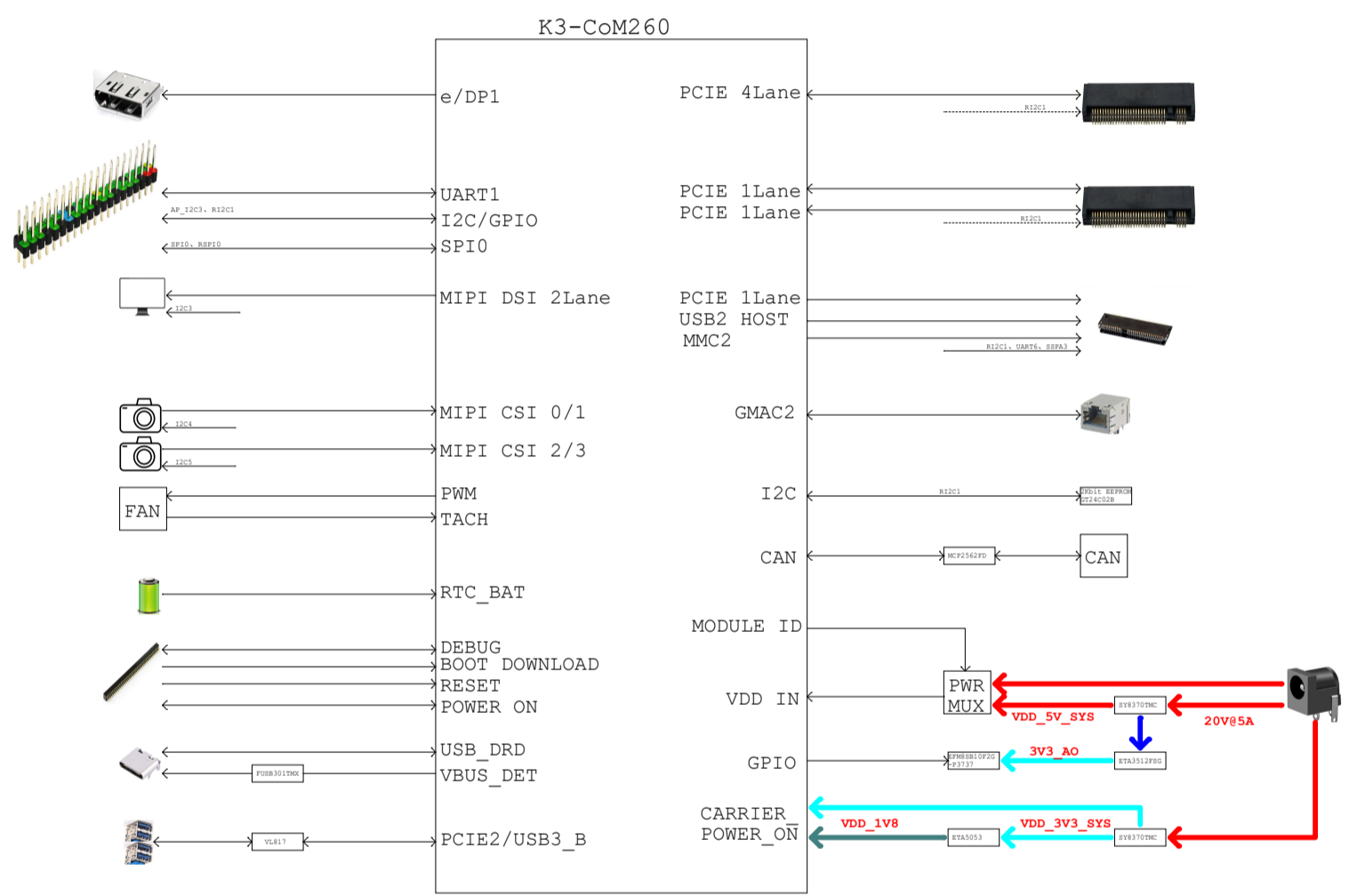
# K3-CoM260\_KIT

Revision	Date	Change Description
V1.0	20250723	Initial version
V2.0	20260306	1.U1608 电源输出近端添加1uF 0402电容 2.U1601 5V输入添加1个1uF和100nF 0201电容 3.Y2201、Y2301、Y2302修改symbol 4.R1508 6.8K改为3.4K 5.R1704 2K改为100K 6.FAN 添加开关 7.UART0 TX和RX调换

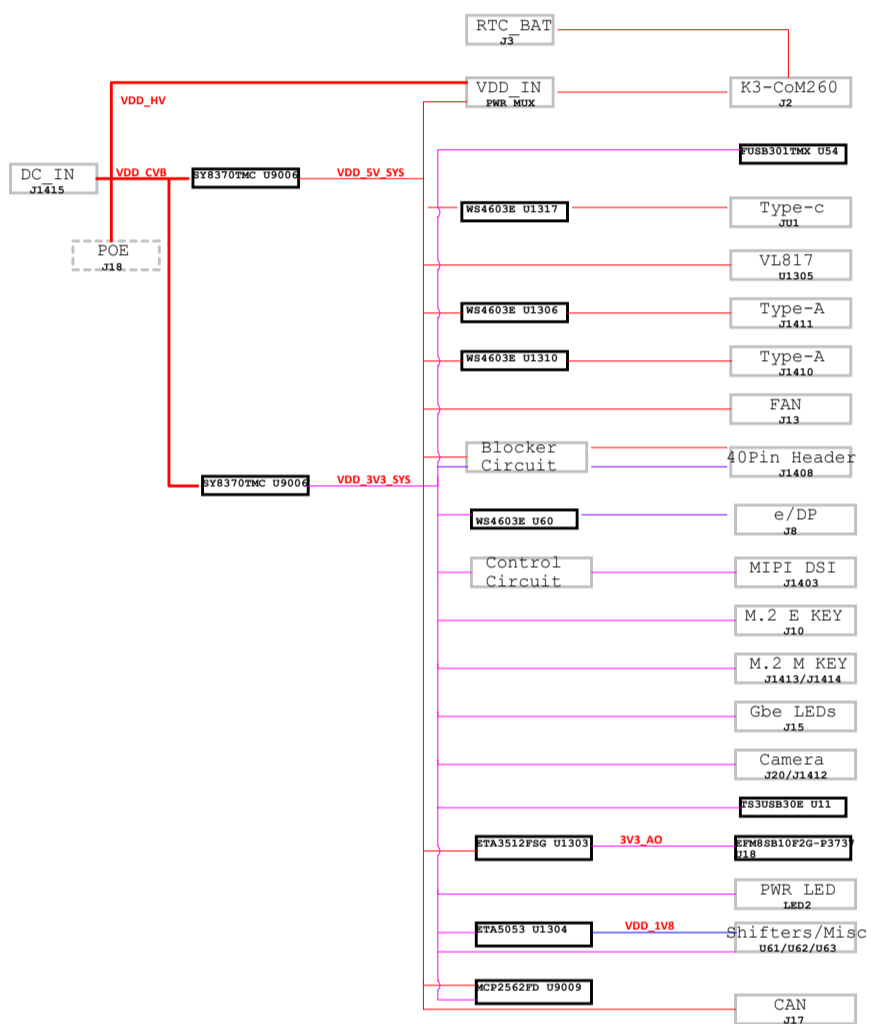
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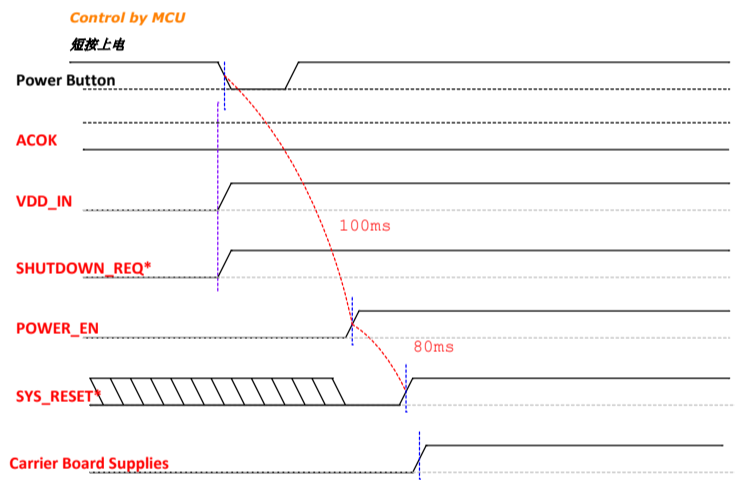
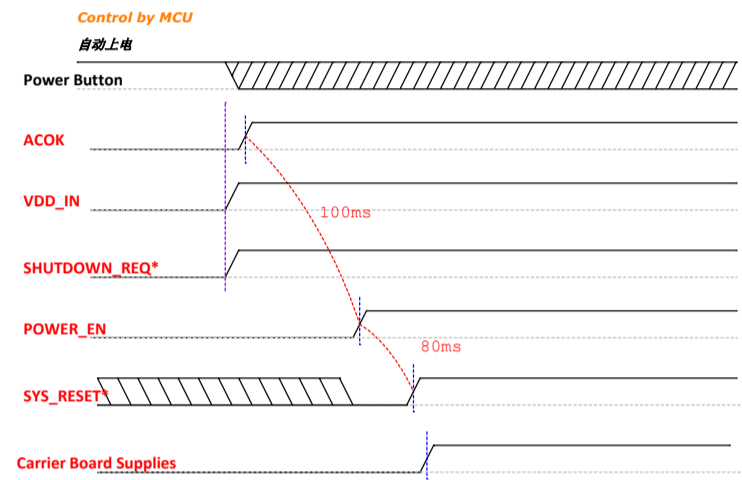
**BLOCK**



# POWER TREE



# POWER SEQUENCE



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**CLOCK MAP**

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**UART MAP**

**SW&RESET MAP**

**PCIE/USB MAP**

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# GPIO ASSIGNMENT

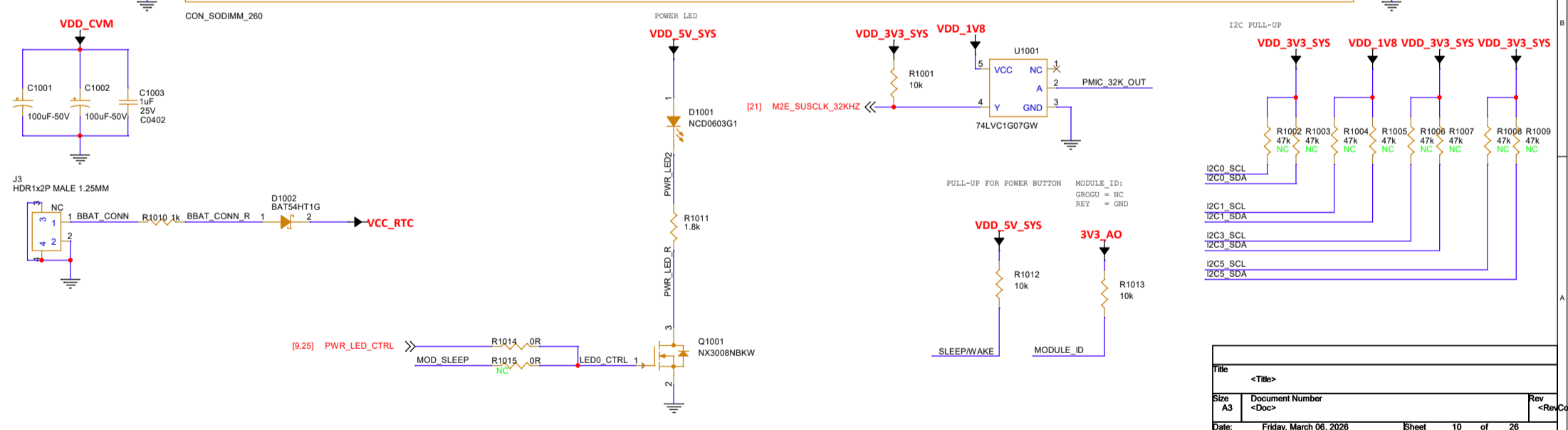
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# SODIMM CONN\_1/2



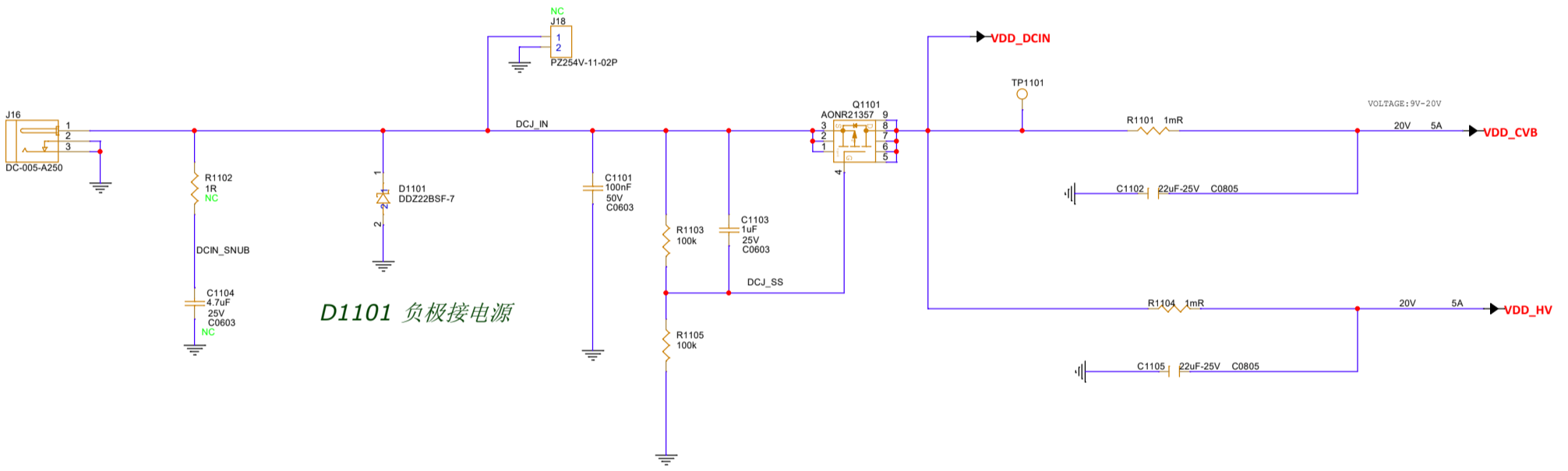
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# SODIMM CONN\_2/2



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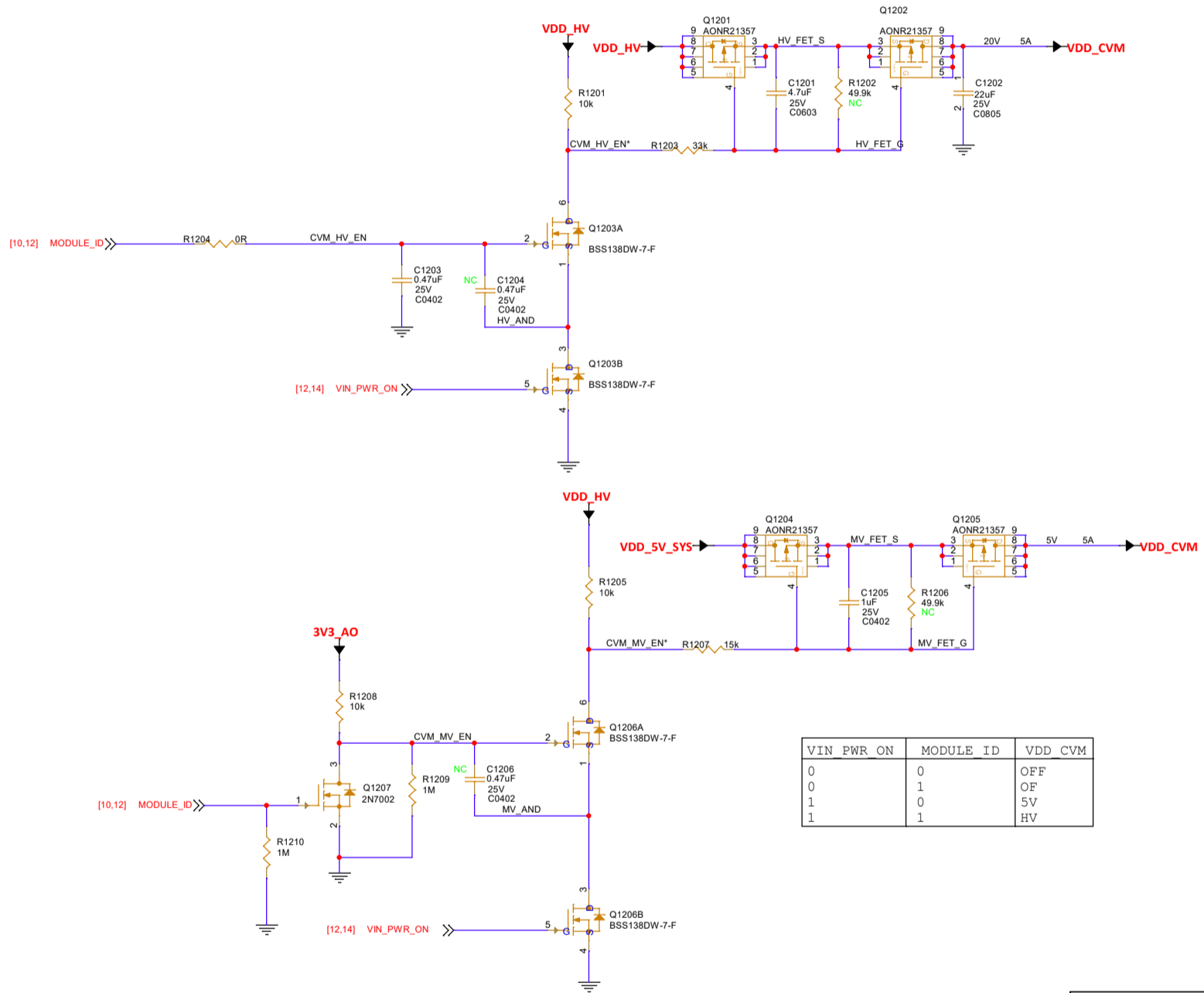
# DC JACK POWER PATH



D1101 负极接电源

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# CVM MV/HV POWER MUX



VIN PWR ON	MODULE ID	VDD CVM
0	0	OFF
0	1	OFF
1	0	5V
1	1	HV

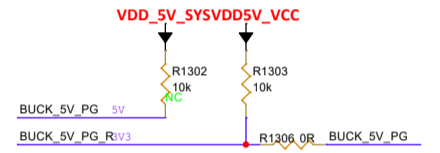
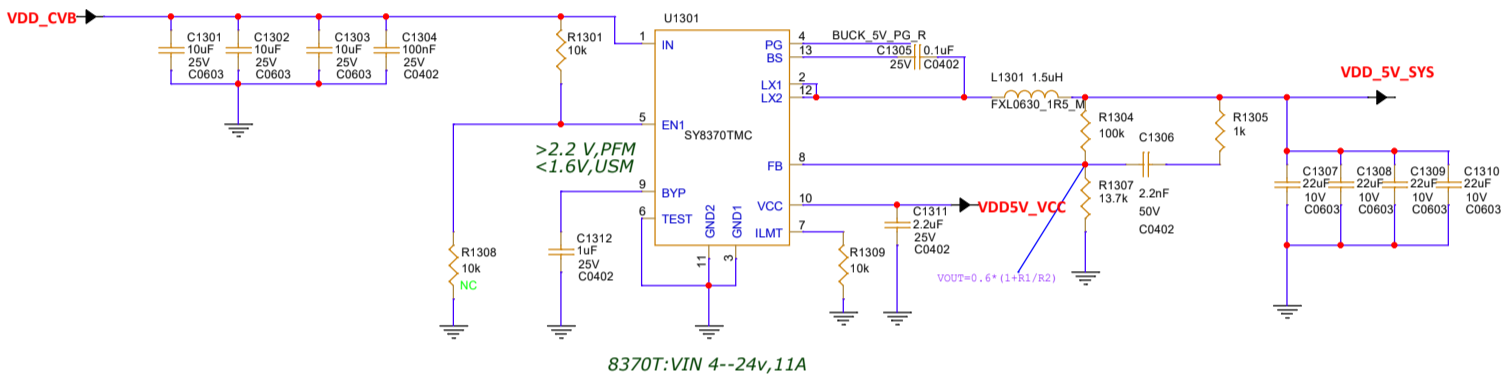
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# EXT DCDC

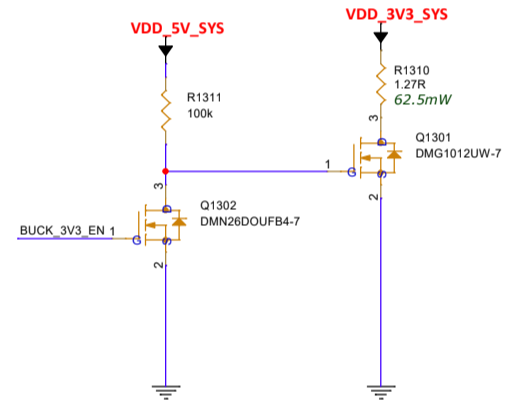
检查封装: U1301, U1302, L1301, L1302, Q1301, Q1302

BUCK\_3V3\_PG >> BUCK\_3V3\_PG [14,16,17,18]  
BUCK\_5V\_PG >> BUCK\_5V\_PG [14]

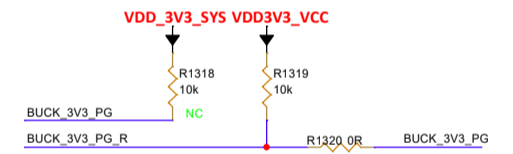
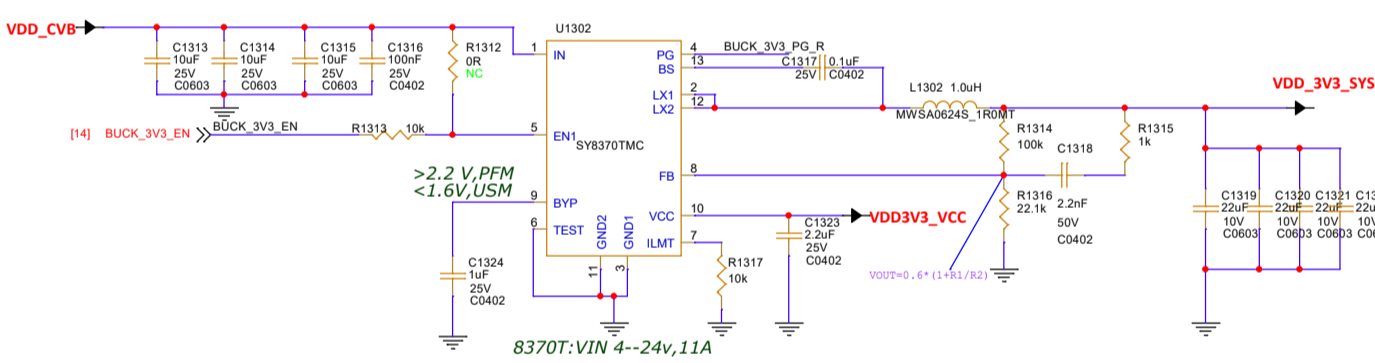
## VDD\_5V\_SYS



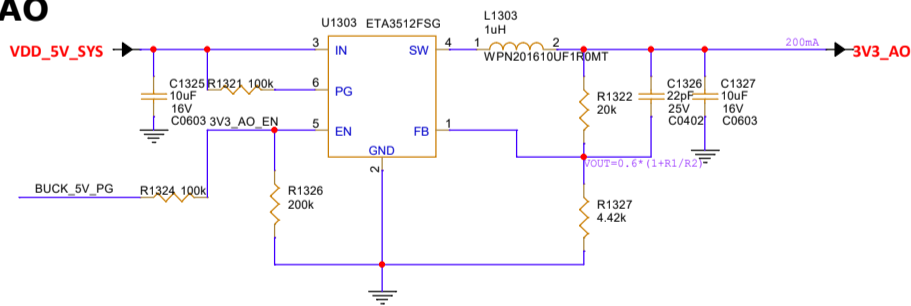
### 快速放电



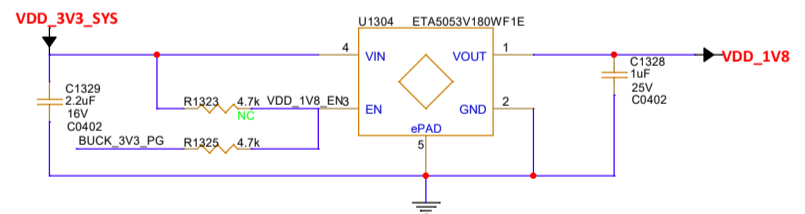
## VDD\_3V3\_SYS



## 3V3\_AO



## VDD\_1V8



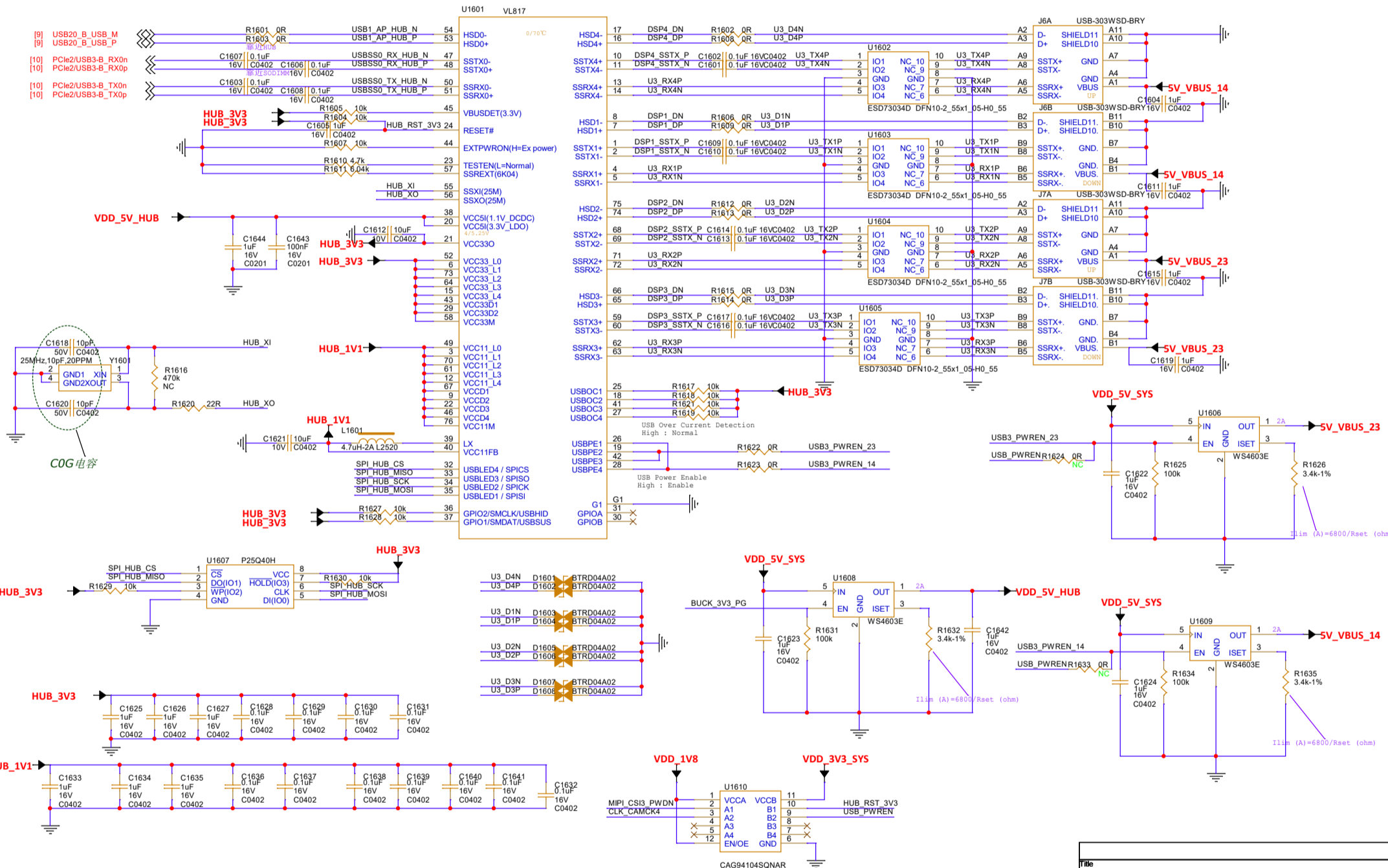
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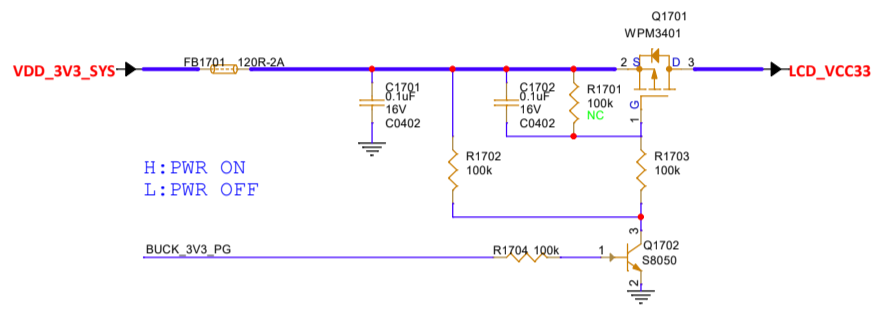
# USB HUB

[13,14,17,18] BUCK\_3V3\_PG >> BUCK\_3V3\_PG  
 [9,19] MIPI\_CSIS\_PWDN >> MIPI\_CSIS\_PWDN  
 [9,19] CLK\_CAMCK4 <<< CLK\_CAMCK4



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# MIPI DSI



[9] MIPI\_DSI1\_LANE1\_DN >> MIPI\_DSI1\_LANE1\_DN  
 [9] MIPI\_DSI1\_LANE1\_DP >> MIPI\_DSI1\_LANE1\_DP

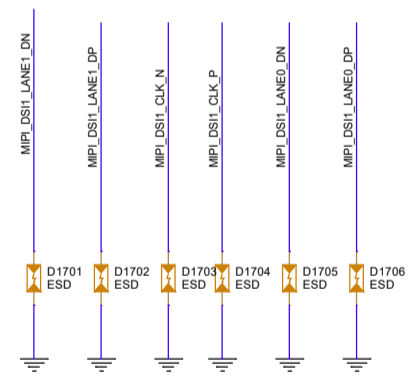
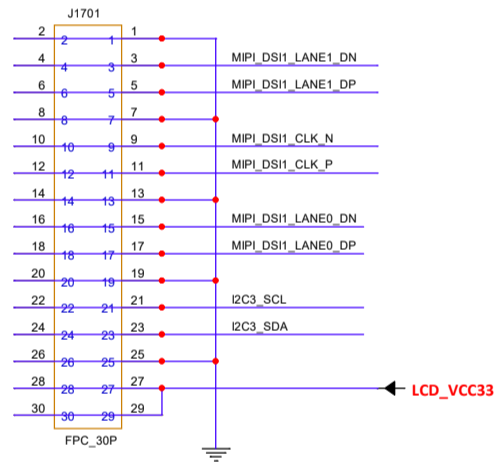
[9] MIPI\_DSI1\_CLK\_N >> MIPI\_DSI1\_CLK\_N  
 [9] MIPI\_DSI1\_CLK\_P >> MIPI\_DSI1\_CLK\_P

[9] MIPI\_DSI1\_LANE0\_DN >> MIPI\_DSI1\_LANE0\_DN  
 [9] MIPI\_DSI1\_LANE0\_DP >> MIPI\_DSI1\_LANE0\_DP

[10,24] I2C3\_SDA >> I2C3\_SDA

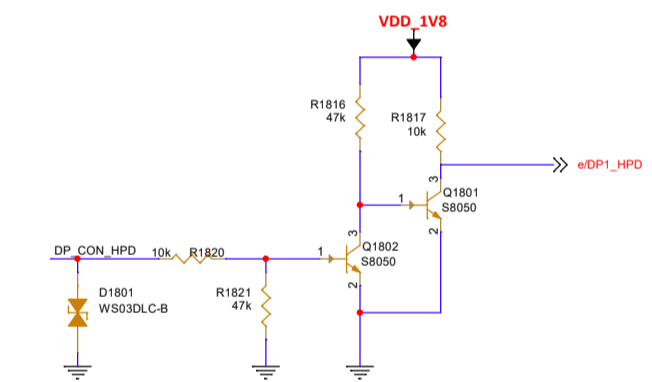
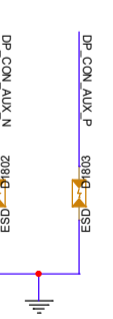
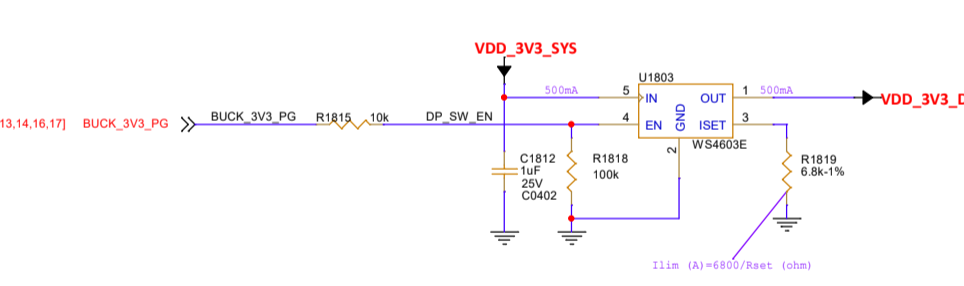
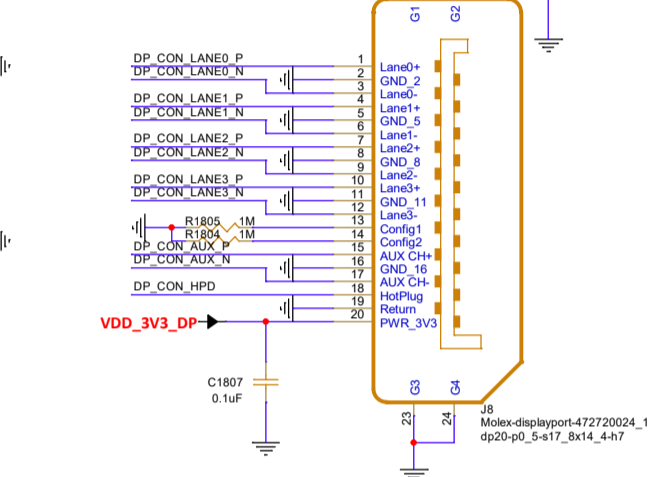
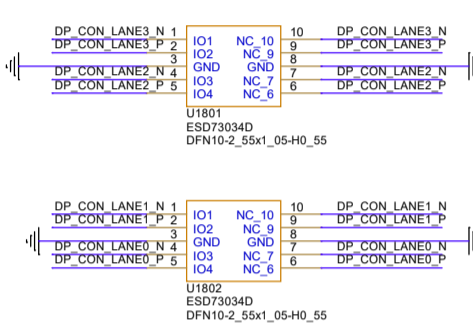
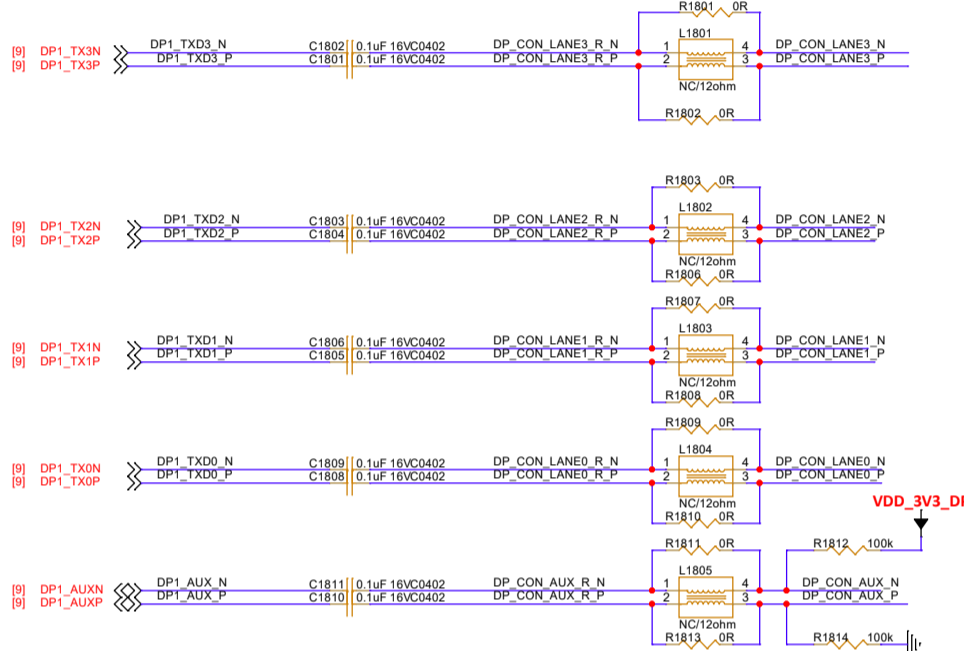
[10,24] I2C3\_SCL >> I2C3\_SCL

[13,14,16,18] BUCK\_3V3\_PG >> BUCK\_3V3\_PG



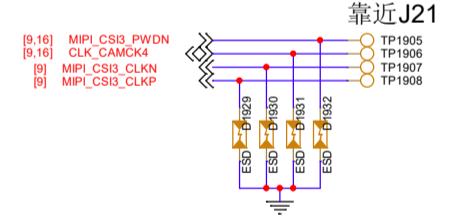
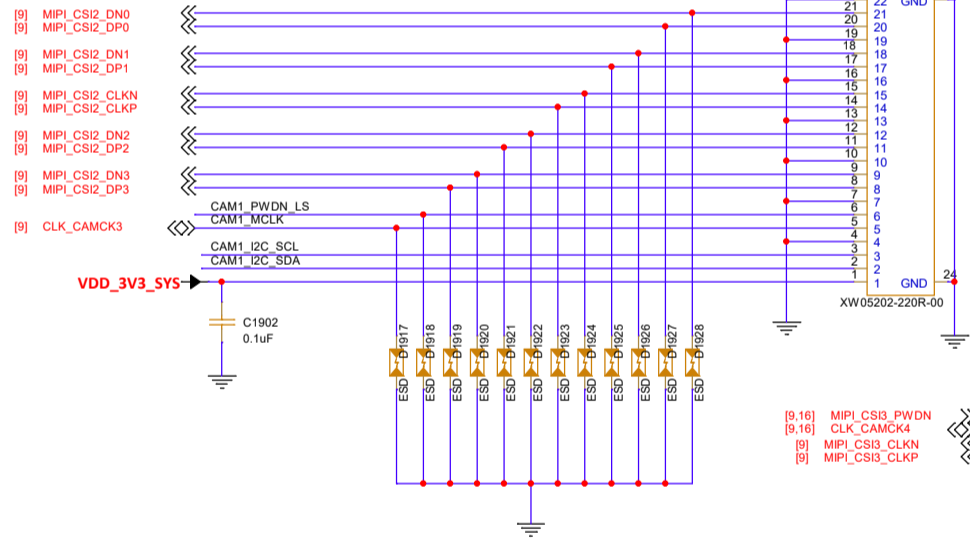
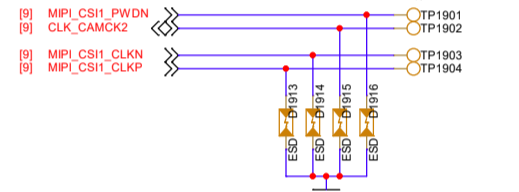
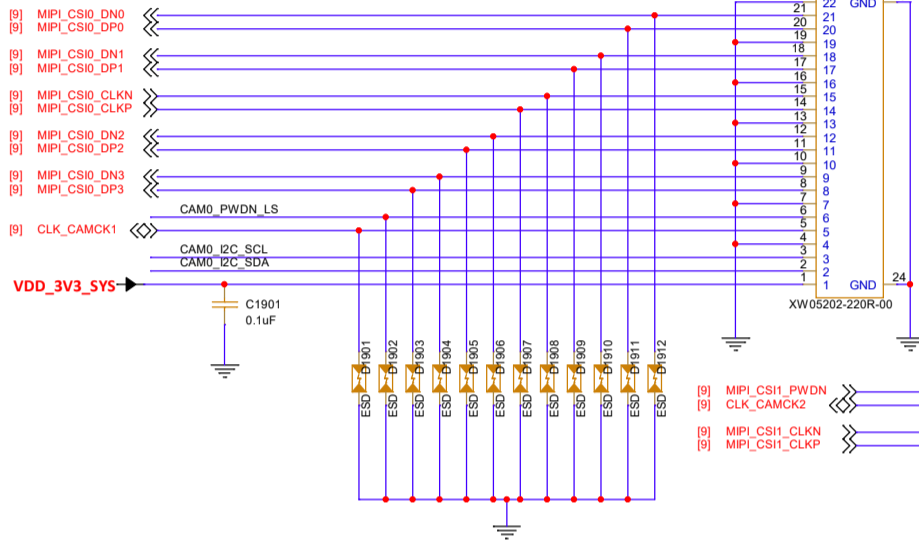
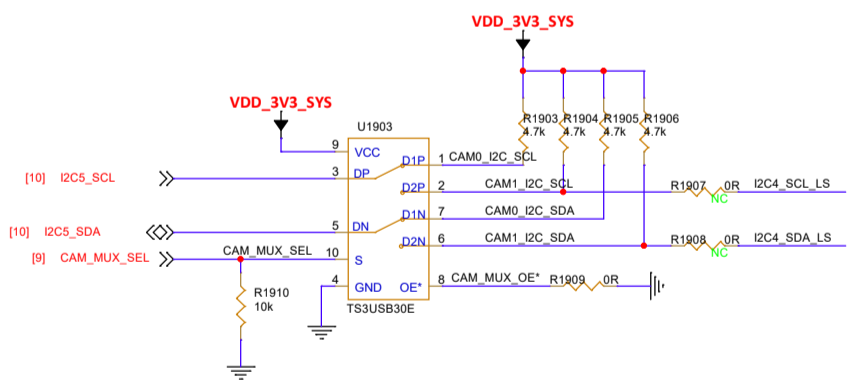
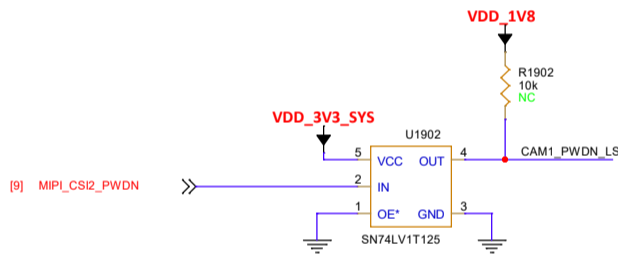
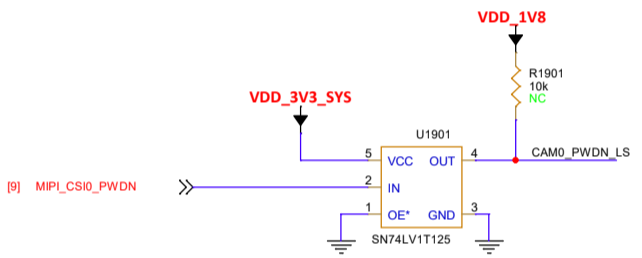
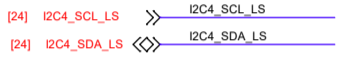
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# DP



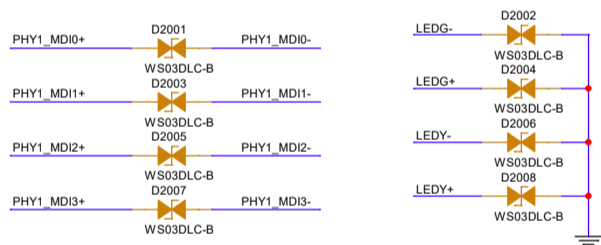
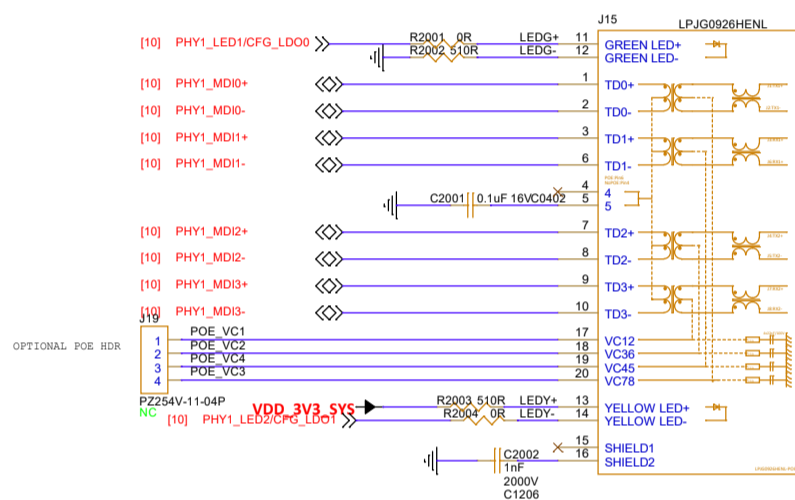
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# CAMERA



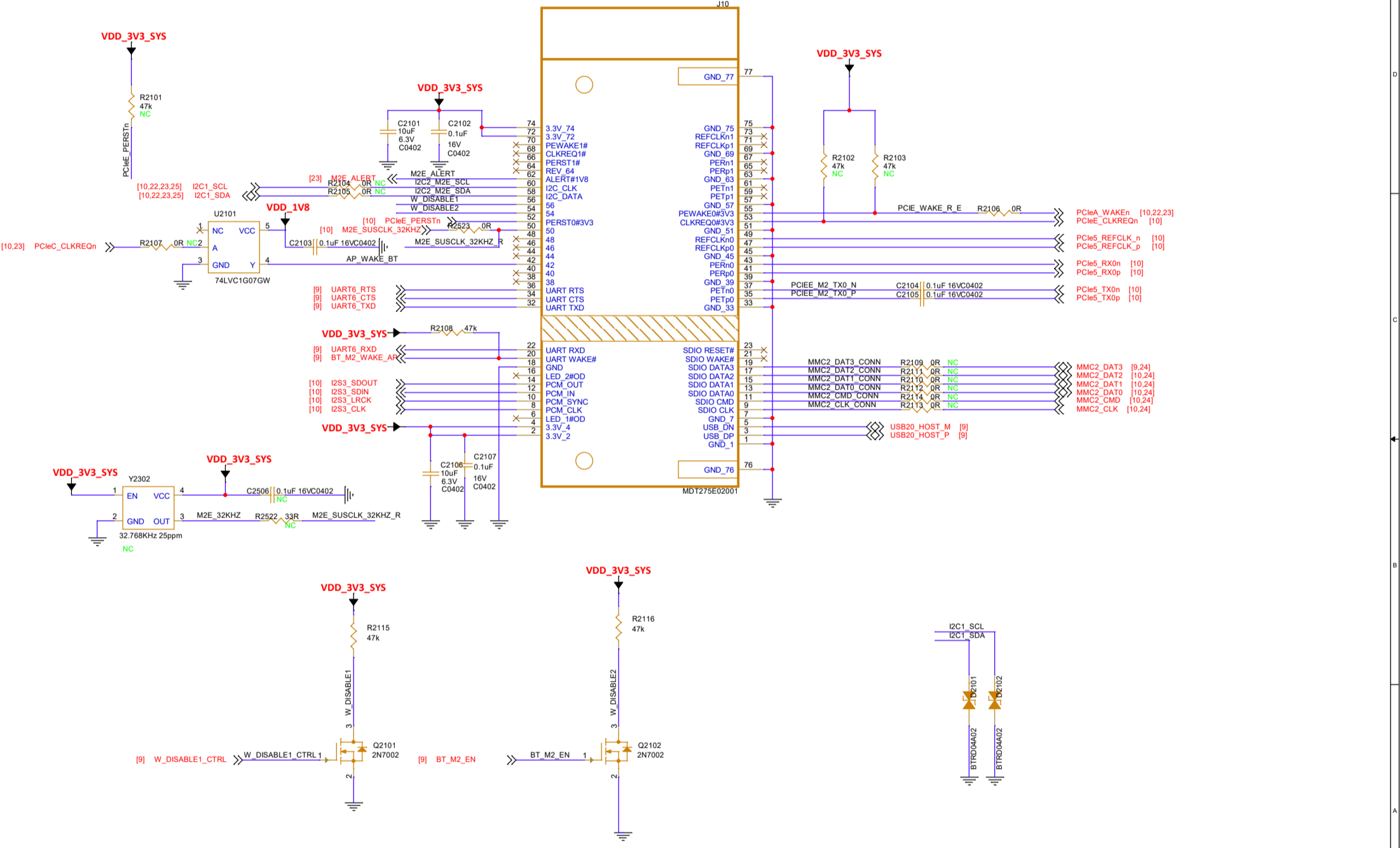
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# GIGABIT ETHERNET W/ POE



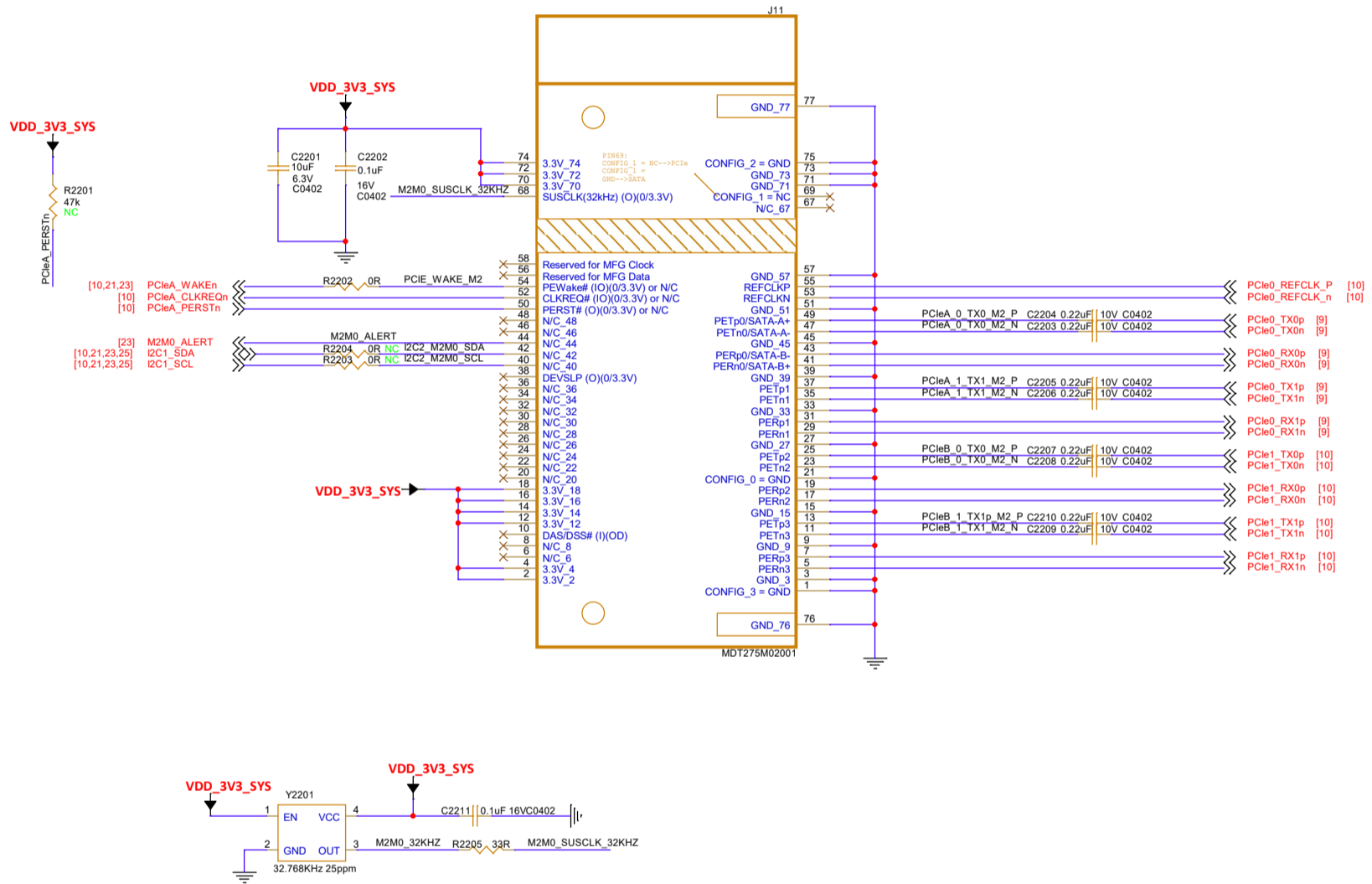
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# M.2 KEY-E\_WIFI\_BT



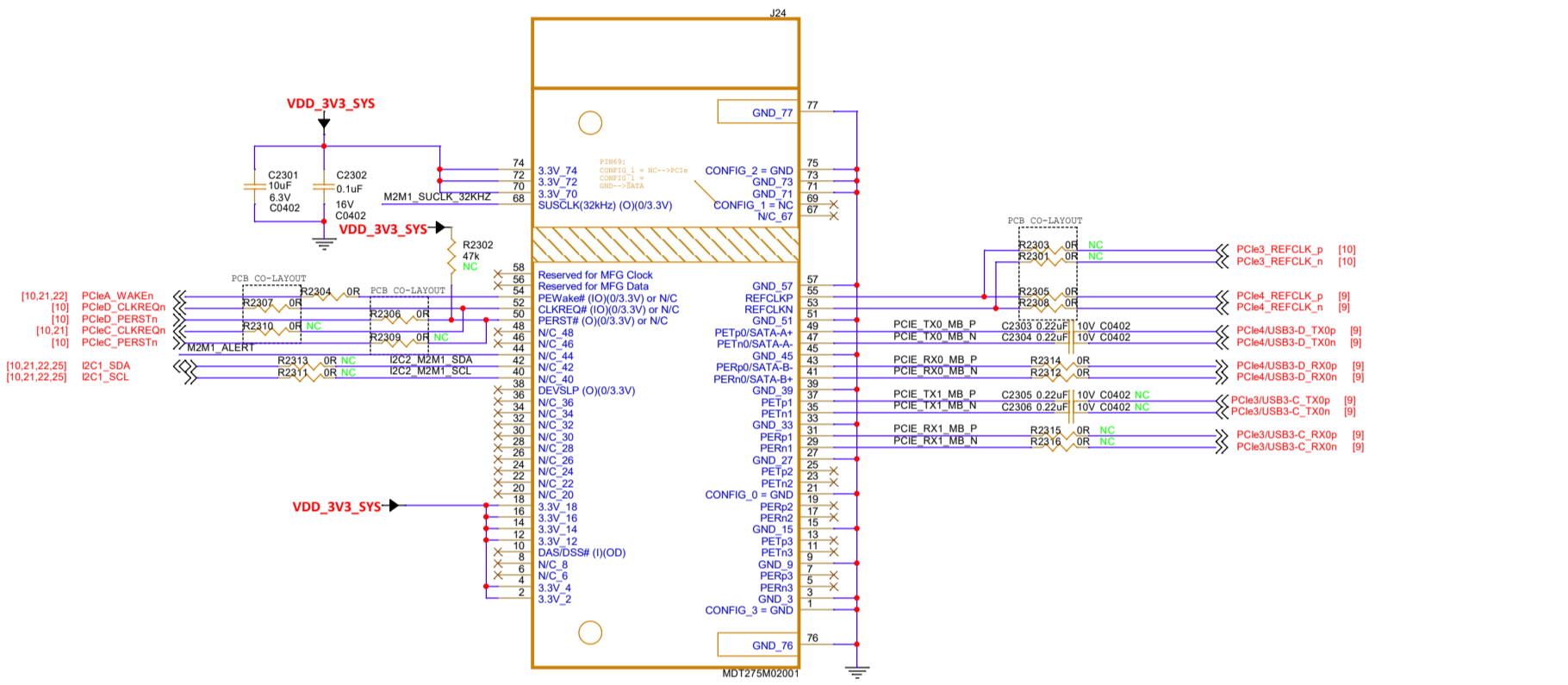
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# M.2 KEY-M \_NVMe\_ [A]

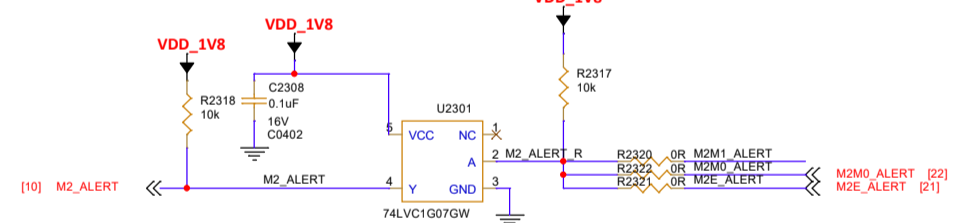
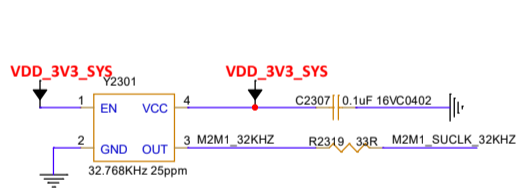


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# M.2 KEY-M\_NVMe\_[B]



## MERGED M.2 ALERTS

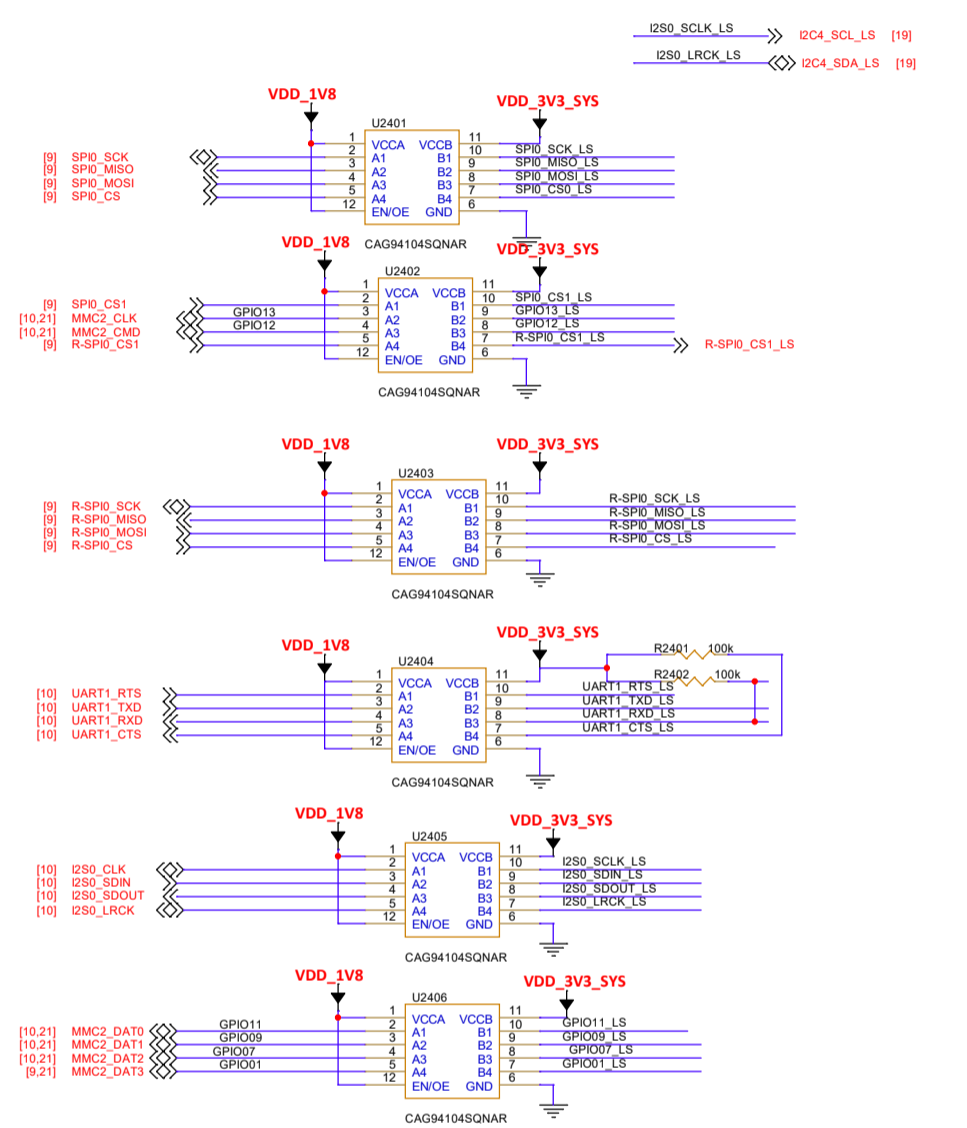
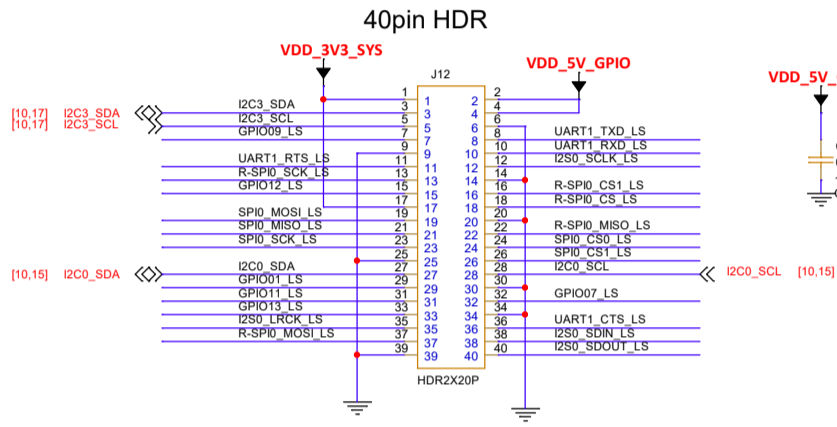


## M.2 Mechanical stand-off



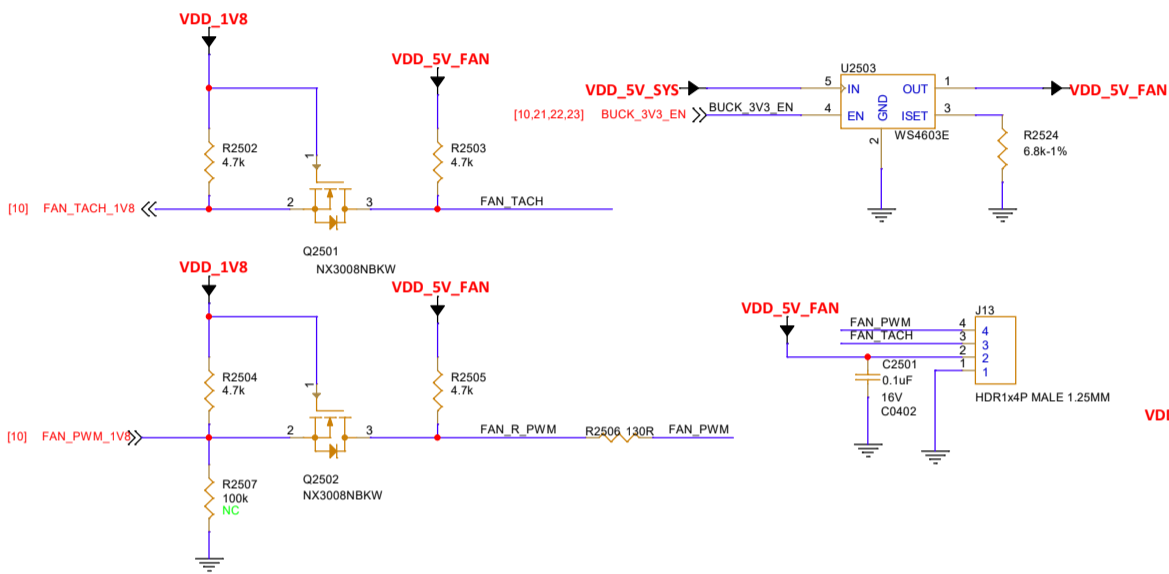
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# 40PIN GPIO Expansion HDR

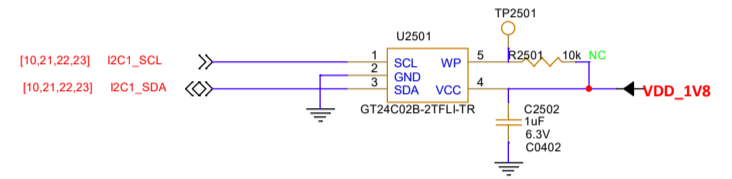


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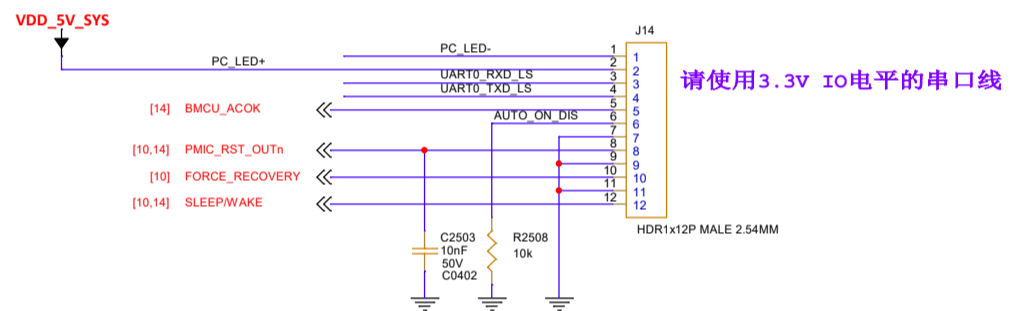
# SoC Fan Header 4pin



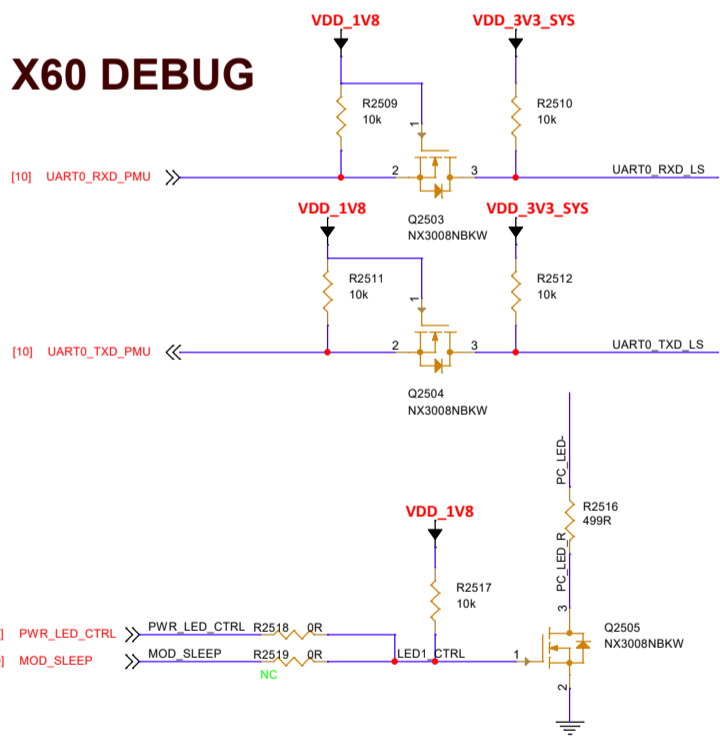
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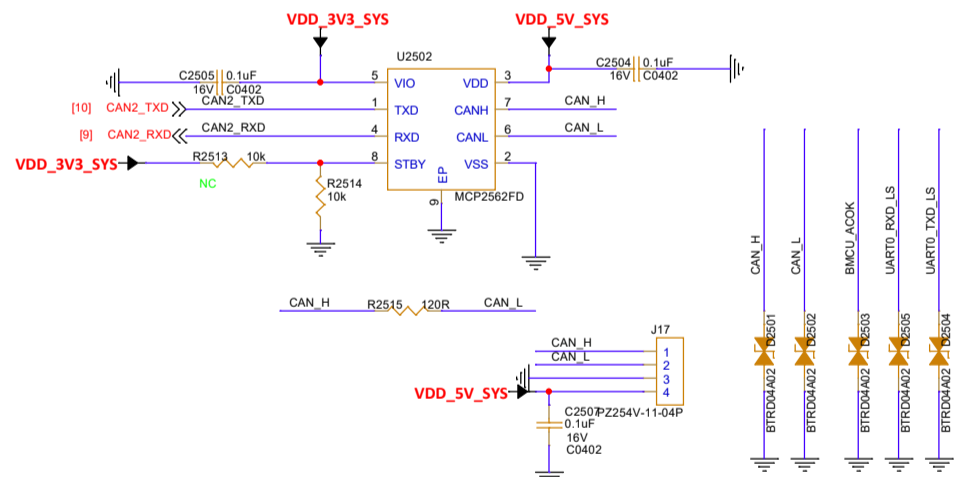
# Button Header Pinout



# X60 DEBUG

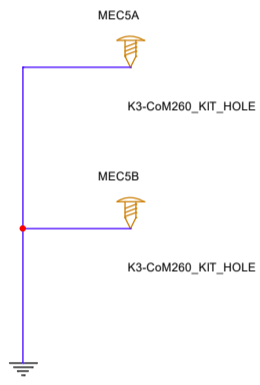


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# Mounting holes for Module



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